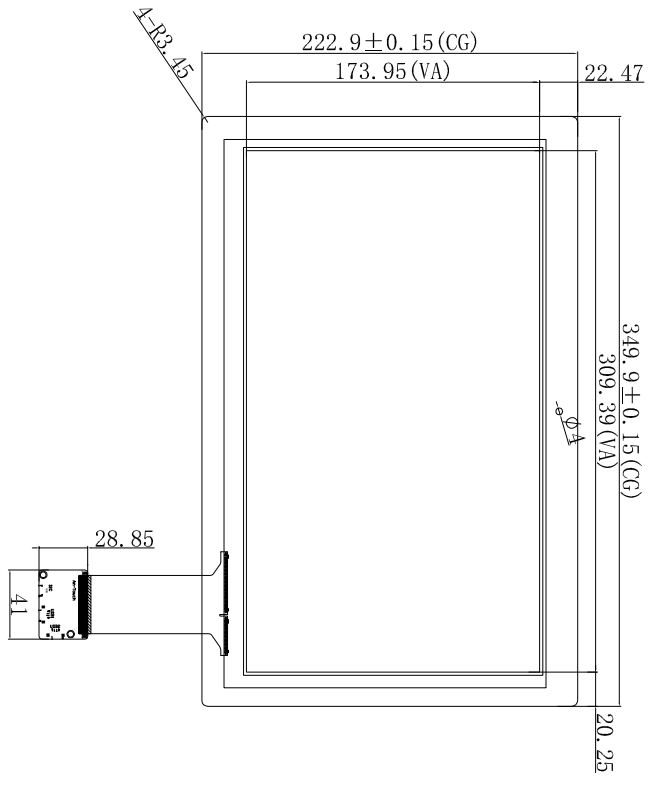
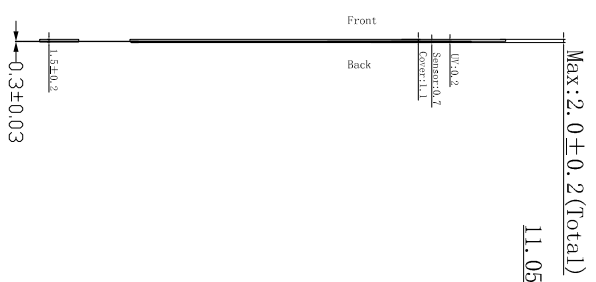


| REV | MARK | HISTORY | DATE | NAME |
|--------|------|---------|------------|------|
| REV. 0 | — | — | 2018.07.17 | JSY |

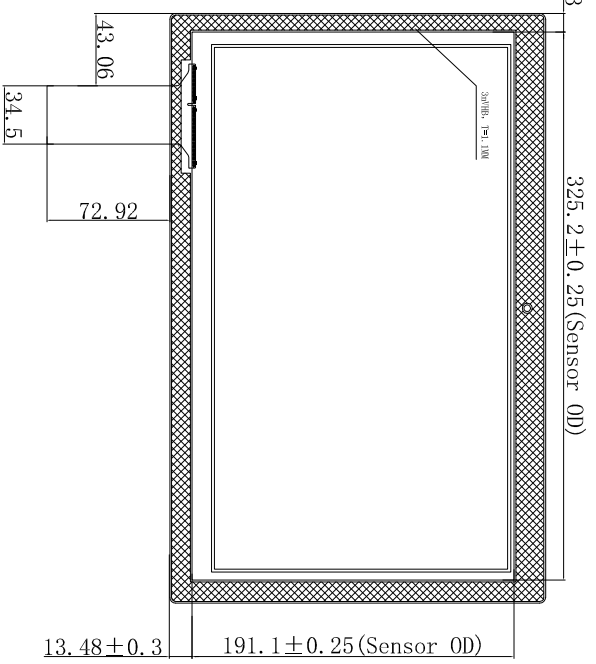
TOP VIEW



SIDE VIEW



BACK VIEW



Notes:

1. TP Structure: G+G;
2. TP Type: COB; IC: IL1TEK2302
3. Transmittance: $\geq 85\%$
4. Hardless: $>6H$
5. Operating conditions: $-10^{\circ}\text{C}^{\sim}+60^{\circ}\text{C}$, $\leq 85\%RH$
6. Storage conditions: $-10^{\circ}\text{C}^{\sim}+65^{\circ}\text{C}$, $\leq 85\%RH$
7. Unspecified tolerance: $\pm 0.2\text{mm}$
8. All materials in the drawing comply with the Rosh

J1 (USB通讯)

| PIN | 定义 |
|-----|--------|
| 1 | VDD/5V |
| 2 | D- |
| 3 | D+ |
| 4 | GND |

J2 (I2C通讯)

| PIN | 定义 |
|-----|-----|
| 1 | S2V |
| 2 | S2V |
| 3 | S2V |
| 4 | GND |
| 5 | GND |
| 6 | RST |

| | | | | | | | |
|--------------------------|------|------|------------|--------|------|-------|----------------------|
| AC-C140001 (USB) DRAWING | | | | SHEET | UNIT | SCALE | 1st ANGLE PROJECTION |
| DESIGN | DATE | DATE | DATE | 1 of 1 | m m | 1 : 1 | |
| CHECK 甲 | JSY | DATE | 2018.07.17 | | | | |